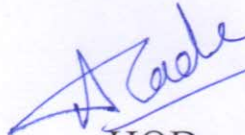




Date : 2/7/2018

**NOTICE**

All the students of SE /TE/BE are hereby informed that Bridge course is scheduled from 5/07/2018 to 6/07 2018 as per the time table displayed on notice board. Follow the given schedule and be present for the same.

  
HOD

(Mrs. Nilima Zade)

**H. O. D.**  
Electronics Dept.  
Padmabhushan Vasantdada Patil Pratishtha,  
College of Engineering  
Mumbai - 400 022

**PADMABHUSHAN VASANTIDADA PATIL PRATISHTHAN'S COLLEGE OF ENGINEERING, SION E. - MUMBAI-22**  
**BRIDGE TIME TABLE**

TITLE : ACADEMIC SCHEDULE (FIRST HALF - 2018 )
DEPT : ELECTRONICS ENGINEERING
W.E.F. : 01/01 / 2018 to 05 / 01 / 2018
CLASS - BE (SEM VIII)
CLASSROOM NO-512

TIME(HRS) DAYS	9:00 to 10:00	10:00 to 11:00	11:00 to 11:20	11:20 to 12:20	12:20 to 13:20	13:20 to 13:50	13:50 to 14:50	14:50 to 15:50	15:50 to 16:50
MONDAY	ANT (/KS)	MEMS (/SND)	B	CMOS VLSI (/DP)	CMOS VLSI (/DP)	L	MCOM (PKB)	MCOM (PKB)	MEMS (/SND)
TUESDAY	MCOM (PKB)	CMOS VLSI (/DP)	R	ANT (/KS)	ANT (/KS)	U	MEMS (/SND)	MEMS (/SND)	CMOS VLSI (/DP)
WEDNESDAY	CMOS VLSI (/DP)	MCOM (PKB)	E	MEMS (/SND)	MEMS (/SND)	N	ANT (/KS)	ANT (/KS)	MCOM (PKB)
THURSDAY	MEMS (/SND)	ANT (/KS)	A	MCOM (PKB)	MCOM (PKB)	C	CMOS VLSI (/DP)	CMOS VLSI (/DP)	ANT (/KS)
FRIDAY	CMOS VLSI (/DP)	ANT (/KS)	K	MCOM (PKB)	ANT (/KS)	H	CMOS VLSI (/DP)	MEMS (/SND)	MEMS (/SND)

SUB	CLASS TEACHER	THEORY	TOTAL
CMOS -CMOS VLSI Design	/DP - Mrs. Dharti Patel	09	09
MEMS - MEMS Technology	/SND - Mrs. S. N. Deshpande	09	09
MCOM - Mobile Communication	PKB-Mr.Pramod K.B	08	08
ANT - Advanced Networking Technologies	/KS - Mrs. Khushboo Singh	09	09
		TOTAL	35

Mrs. Sushil Pakhmode /Mrs. Anjali Paisare  
(Time Table Committee Member)  
FYPP- DM- ELEX -FF-01/A Revision 01 07/07/2017

*(Signature)*  
Mrs. Nilima Zade  
(I/C HOD ELEX)

*(Signature)*  
Dr. Alam N. Shaikh  
(PRINCIPAL)





**PADMABHUSHAN VASANTDADA PATIL PRATISTHAN'S COLLEGE OF ENGINEERING, SION E. - MUMBAI-22**  
**BRIDGE TIME TABLE**


TITLE : ACADEMIC SCHEDULE (FIRST HALF - 2018 )
DEPT : ELECTRONICS ENGINEERING
W.E.F. : 08/01 / 2018
CLASS - SE (SEM IV)
CLASSROOM NO-506

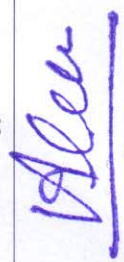
TIME(HRS) DAYS	9:00 to 10:00	10:00 to 11:00	11:00 to 11:20	11:20 to 12:20	12:20 to 13:20	13:20 to 13:50	13:50 to 14:50	14:50 to 15:50	15:50 to 16:50
MONDAY	DSD (/DP)	EDC-II (PM)	B	PCE (/PG)	AM-IV (AYS)	L	LCS (/JP)	MPA (/KW)	MPA (MINI PROJ) (/KW)
TUESDAY	PCE (/PG)	AM-IV (AYS)	R	LCS (/JP)	EDC-II (PM)	U	DSD (/DP)	MPA (/KW)	EDC-II(MINI PROJ) (PM)
WEDNESDAY	AM-IV (AYS)	PCE (/PG)	E	MPA (/KW)	DSD (/DP)	N	EDC-II (PM)	LCS (/JP)	DSD (MINI PROJ) (/DP)
THURSDAY	DSD (/DP)	PCE (/PG)	A	AM-IV (AYS)	MPA (/KW)	C	EDC-II (PM)	LCS (/JP)	PCE(MINI PROJ) (/PG)
FRIDAY	EDC-II (PM)	MPA (/KW)	K	AM-IV (AYS)	LCS (/JP)	H	PCE (/PG)	DSD (/DP)	AM-IV (AYS)

SUB	CLASS TEACHER	THEORY	TUTORIAL	TOTAL
Applied Mathematics IV- (AM-IV)	AYS-Mr. A Y Shete	06	-	06
Electronic Devices and Circuits II -(EDCII)	PM -Mr. P.M. Manegopale	06	-	06
Microprocessors and Applications-(MPA)	/KW- Mrs.Kavita Wagh	06	-	06
Digital System Design-(DSD)	/DP -Mrs. Dharti Patel	06	-	06
Principles of Communication Engineering-(PCE)	/PG-Mrs. Priya Gupta	06	-	06
Linear Control Systems-(LCS)	/JP - Mrs. JayshreePawar	06	-	06
			TOTAL	30

  
 Mrs. Sonali Pakhmode /Mrs. Anjali Pansare  
 (Time Table Committee Member)  
 PVPPP- DM- ELEX -FF-04/A Revision 01 07/07/2017

  
 Mrs. Gitanjali Korgaonkar  
 (Time Table Committee Convener)

  
 Mrs. Nilima Zade  
 (I/C HOD ELEX)

  
 Dr. Alan N. Shaikh  
 (PRINCIPAL)

FH-18-TE

**PADMABHUSHAN VASANTDADA PATIL PRATISHTHAN'S COLLEGE OF ENGINEERING, SION E. - MUMBAI-22**  
**BRIDGE TIME TABLE**

**EH-18-SE**

TITLE : ACADEMIC SCHEDULE (FIRST HALF - 2018 )

DEPT : ELECTRONICS ENGINEERING


W.E.F. : 01/01/2018 to 05/01/2018

CLASS - SE (SEM IV)

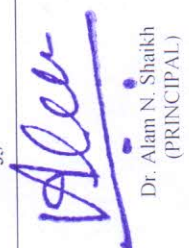
CLASSROOM NO-506

TIME(HRS) DAYS	9:00 to 10:00	10:00 to 11:00	11:00 to 11:20	11:20 to 12:20	12:20 to 13:20	13:20 to 13:50	13:50 to 14:50	14:50 to 15:50	15:50 to 16:50
MONDAY	DSD (/DP)	EDC-II (PM)	B	PCE (/PG)	AM-IV (AYS)	L	LCS (/JP)	MPA (/KW)	MPA (/KW)
TUESDAY	PCE (/PG)	AM-IV (AYS)	R	LCS (/JP)	EDC-II (PM)	U	DSD (/DP)	MPA (/KW)	EDC-II (PM)
WEDNESDAY	AM-IV (AYS)	PCE (/PG)	E	MPA (/KW)	DSD (/DP)	N	EDC-II (PM)	LCS (/JP)	DSD (/DP)
THURSDAY	DSD (/DP)	PCE (/PG)	A	AM-IV (AYS)	MPA (/KW)	C	EDC-II (PM)	LCS (/JP)	PCE (/PG)
FRIDAY	EDC-II (PM)	MPA (/KW)	K	AM-IV (AYS)	LCS (/JP)	H	PCE (/PG)	DSD (/DP)	AM-IV (AYS)

SUB	CLASS TEACHER	THEORY	TUTORIAL	TOTAL
Applied Mathematics IV- (AM-IV)	AYS-Mr. A Y Shete	06	-	06
Electronic Devices and Circuits II -(EDCII)	PM-Mr. P.M. Manegopale	06	-	06
Microprocessors and Applications- (MPA)	/KW- Mrs.Kavita Wagh	06	-	06
Digital System Design-(DSD)	/DP -Mrs. Dharti Patel	06	-	06
Principles of Communication Engineering-(PCE)	/PG-Mrs. Priya Gupta	05	-	05
Linear Control Systems-(LCS)	/JP - Mrs. JayshreePawar	06	-	06
			TOTAL	35

  
 Mrs. Gitanjali Korgaonkar  
 (Time Table Committee Convener)

  
 Mrs. Nilima Zade  
 (I/C HOD ELEX)

  
 Dr. Alam N. Shaikh  
 (PRINCIPAL)

Mrs.Sonali Pakhmode /Mrs.Anjali Pansare  
 (Time Table Committee Member)  
 PAPP- DM- ELEX -FF-04/A Revision 01 07/07/2017





Date : 26/12/2017

## NOTICE

All the students of SE /TE/BE electronics are hereby informed that Bridge course is scheduled from 01/01/2018 to 5/01 2018 as per the time table displayed on notice board of respective class room. Follow the given schedule.

HOD

(Mrs. Nilima Zade)

**H. O. D.**  
Electronics Dept.  
Padmabhushan Jasantojba Patil Pratishtha  
College of Engineering  
Mumbai - 400 022